	•						SHEET	Г1 (OF 1	
Substitute for	form 1449A/PTO •	ATTORNEY'S DKT NO. 032481-021			APPLICATION NO. 09/687,777					
	THIRD SUF	APPLICANT Mukesh Patel								
INF	FORMATIO	FILING DATE GROUP								
*STATEMENT BY APPLICANT				October 13, 2000			2899			
2002										
AND BY	·		U.S. PATENT DO	DEUMENTS						
OK S.	U.S. Patent Document		_							
Examiner Initials	Number (if known)		Name of Patentee or Applicant of Cited Document		t	Date of Publication (MM-DD-YYYY)				
		<u> </u>	 			 _				
	 					 	RECEIVE	<u>D</u>		
		+				 ,	JUL 2 9 200	2	-	
							Technology Center 2100			
								-100		
		<u> </u>				igsqcup				
		<u> </u>	ļ			↓				
						┼─				
	 			·		 				
						 				
		FC	DREIGN PATENT	DOGUMENTS		4				
	Foreign Patent Document									
Examiner Initials	Number	Kind Code (if known)	Count	I		Pate of Publication (MM-DD-YYYY)		Translation Yes no		
				,				100	, <u>.</u>	
· · · · · · · · · · · · · · · · · · ·		<u> </u>		 	<u> </u>			<u> </u>		
-		<u> </u>						<u> </u>	ļ	
	 				 			 '	 	
	1	NON P	ATENT LITERATU	URE DOCUME	L Nets				- 1	
	Include		• • • • • • • • • • • • • • • • • • • •	<u> </u>		n appr	opriate), title of th	 ne		
Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.									
CD	C. JOHN GLOSSNER and STAMATIS VASSILIADIS, <i>The Delft-Java Engine: An Introduction</i> , Euro-Part '97, Parallel Processing, Third International Euro-Par Conference, pp. 766-770 (August 1997)									
CD	M.W. EL-KHARASHI et al., <i>Java Microprocessor: Computer Architecture Implications</i> , IEEE Pacific Rim Conference on Communications, Computers and Signal Processing, Vol. 1, pp. 277-280 (August 20-22, 1997)									

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.

R. TOMASULO, An Efficient Algorithm for Exploring Multiple Arithmetic Units, IBM Journal of Research and

Date

Considered

Development, Vol. 11, No. 1, pp. 25-33 (January 1967)

Examiner

Signature